

N-Channel Enhancement Mode Power MOSFET

Description

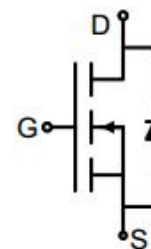
The G3404LL-BQ uses advanced trench technology to provide excellent $R_{DS(ON)}$, low gate charge. It can be used in a wide variety of applications.

General Features

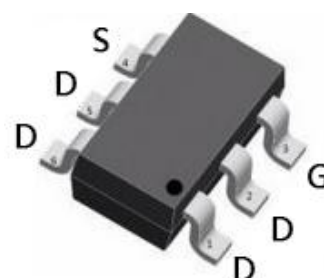
- V_{DS} 30V
- I_D (at $V_{GS} = 10V$) 6A
- $R_{DS(ON)}$ (at $V_{GS} = 10V$) < 22m Ω
- $R_{DS(ON)}$ (at $V_{GS} = 4.5V$) < 35m Ω
- 100% Avalanche Tested
- RoHS Compliant

Application

- Power switch
- DC/DC converters



Schematic diagram



SOT-23-6L

Ordering Information

Device	Package	Marking	Packaging
G3404LL-BQ	SOT-23-6	G3404	3000psc/Reel

Absolute Maximum Ratings $T_C = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Value	Unit
Drain-Source Voltage	V_{DS}	30	V
Continuous Drain Current	I_D	6	A
Pulsed Drain Current (note1)	I_{DM}	24	A
Gate-Source Voltage	V_{GS}	± 20	V
Power Dissipation	P_D	1.2	W
Operating Junction and Storage Temperature Range	T_J, T_{stg}	-55 To 150	$^\circ\text{C}$

Thermal Resistance

Parameter	Symbol	Value	Unit
Thermal Resistance, Junction-to-Ambient	R_{thJA}	104	$^\circ\text{C/W}$

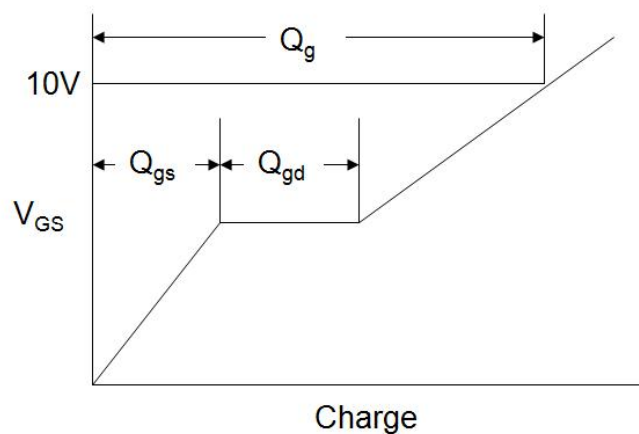
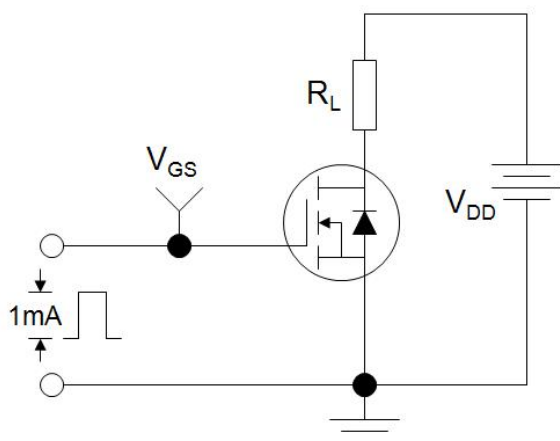
Specifications $T_J = 25^\circ\text{C}$, unless otherwise noted

Parameter	Symbol	Test Conditions	Value			Unit
			Min.	Typ.	Max.	
Static Parameters						
Drain-Source Breakdown Voltage	V _{(BR)DSS}	V _{GS} = 0V, I _D = 250μA	30	--	--	V
Zero Gate Voltage Drain Current	I _{DSS}	V _{DS} = 30V, V _{GS} = 0V	--	--	1	μA
Gate-Source Leakage	I _{GSS}	V _{GS} = ±20V	--	--	±100	nA
Gate-Source Threshold Voltage	V _{GS(th)}	V _{DS} = V _{GS} , I _D = 250μA	1	1.5	2	V
Drain-Source On-Resistance	R _{DS(on)}	V _{GS} = 10V, I _D = 4.2A	--	16	22	mΩ
		V _{GS} = 4.5V, I _D = 4A	--	23	35	
Forward Transconductance	g _{FS}	V _{GS} = 5V, I _D = 4.2A	--	12	--	S
Dynamic Parameters						
Input Capacitance	C _{iss}	V _{GS} = 0V, V _{DS} = 15V, f = 1.0MHz	--	541	--	pF
Output Capacitance	C _{oss}		--	70	--	
Reverse Transfer Capacitance	C _{rss}		--	55	--	
Total Gate Charge	Q _g	V _{DD} = 15V, I _D = 4.2A, V _{GS} = 10V	--	12.2	--	nC
Gate-Source Charge	Q _{gs}		--	2.4	--	
Gate-Drain Charge	Q _{gd}		--	2.3	--	
Turn-on Delay Time	t _{d(on)}	V _{DD} = 15V, I _D = 4.2A, R _G = 3Ω	--	28	--	ns
Turn-on Rise Time	t _r		--	5	--	
Turn-off Delay Time	t _{d(off)}		--	22	--	
Turn-off Fall Time	t _f		--	13	--	
Drain-Source Body Diode Characteristics						
Continuous Body Diode Current	I _S	T _C = 25°C	--	--	6	A
Body Diode Voltage	V _{SD}	T _J = 25°C, I _{SD} = 4.2A, V _{GS} = 0V	--	--	1.2	V
Reverse Recovery Charge	Q _{rr}	I _F = 4.2A, V _{GS} = 0V di/dt=100A/us	--	2.6	--	nC
Reverse Recovery Time	T _{rr}		--	8.5	--	ns

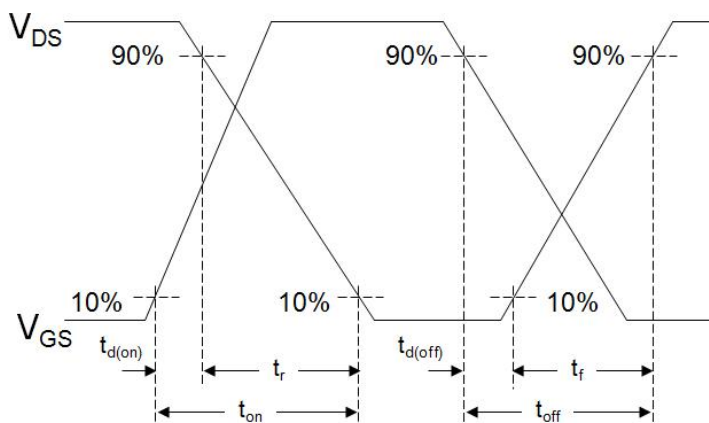
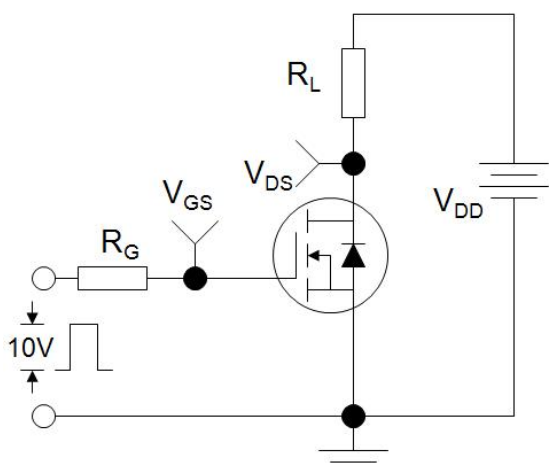
Notes

1. Repetitive Rating: Pulse width limited by maximum junction temperature
2. Identical low side and high side switch with identical R_G

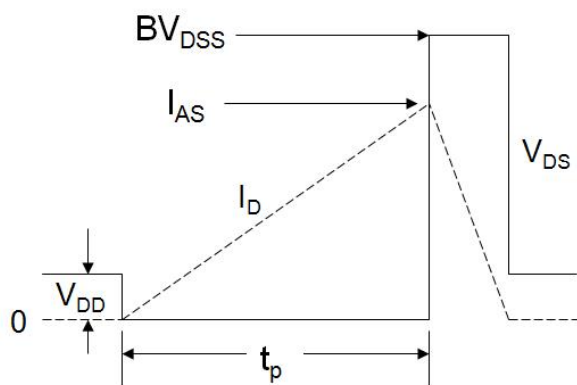
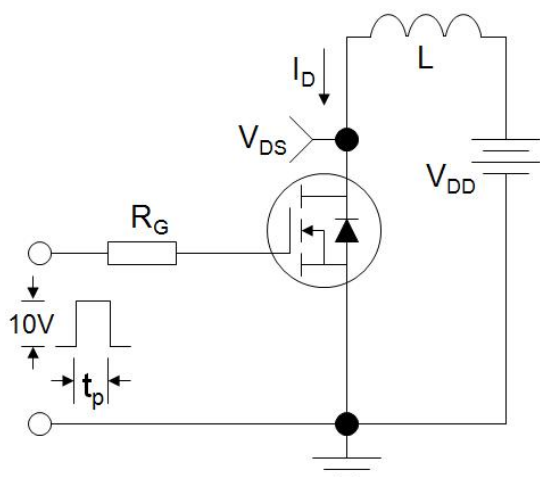
Gate Charge Test Circuit



Switch Time Test Circuit



EAS Test Circuit



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 1. Output Characteristics

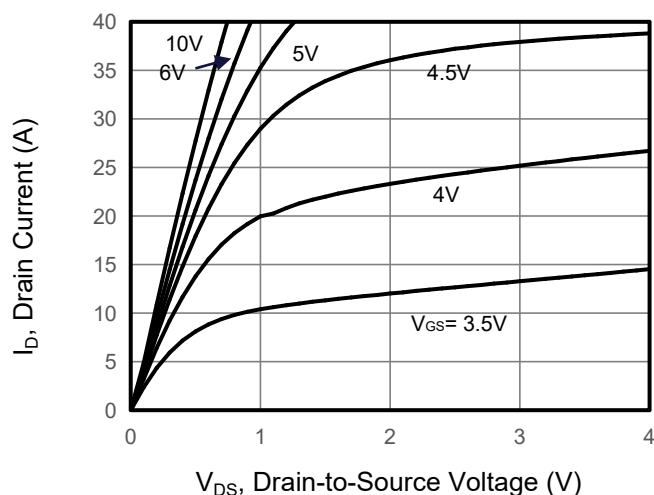


Figure 2. Transfer Characteristics

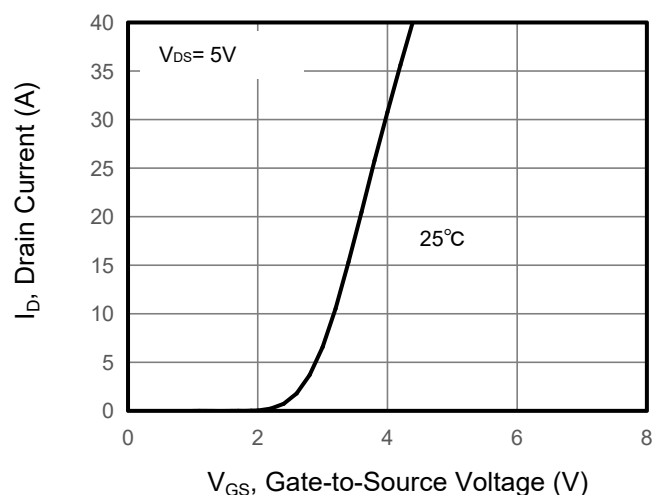


Figure 3. Drain Source On Resistance

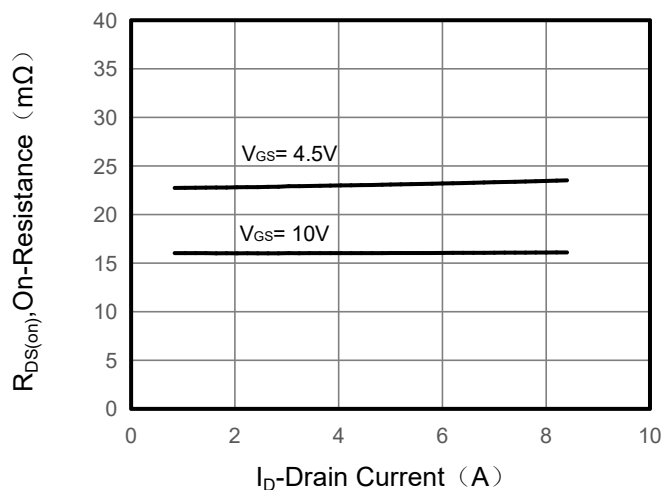


Figure 4. Gate Charge

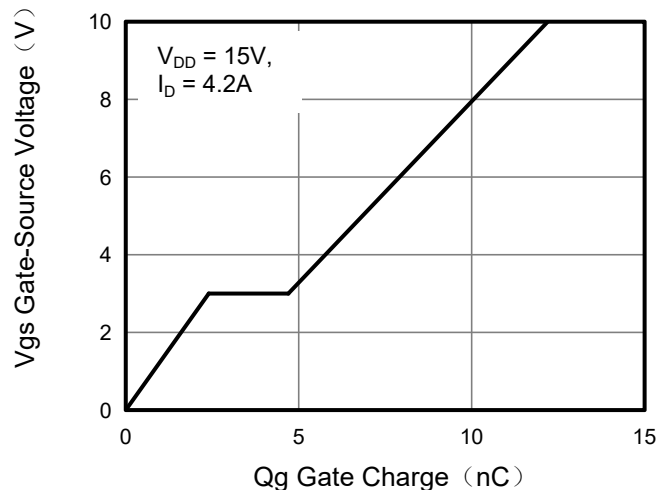


Figure 5. Capacitance

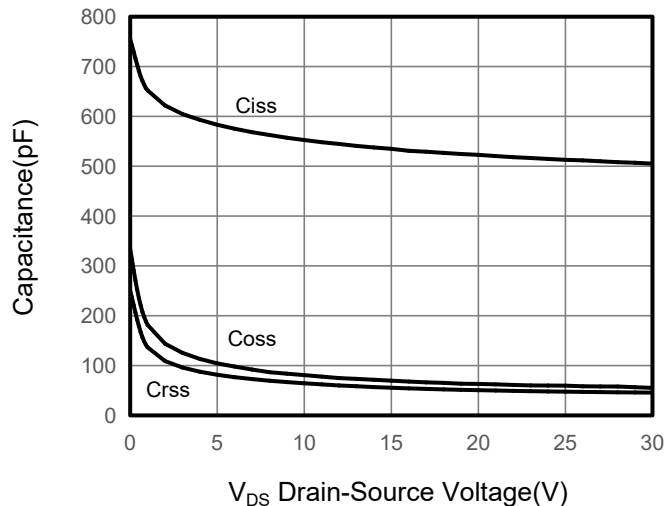
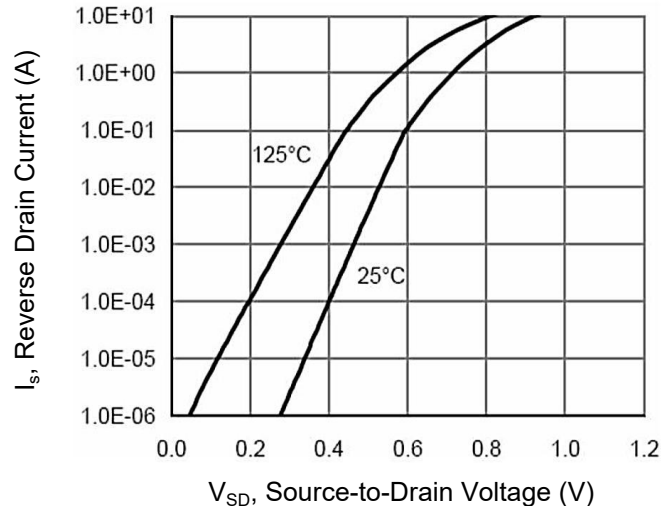


Figure 6. Source-Drain Diode Forward



Typical Characteristics $T_J = 25^\circ\text{C}$, unless otherwise noted

Figure 7. Drain-Source On-Resistance

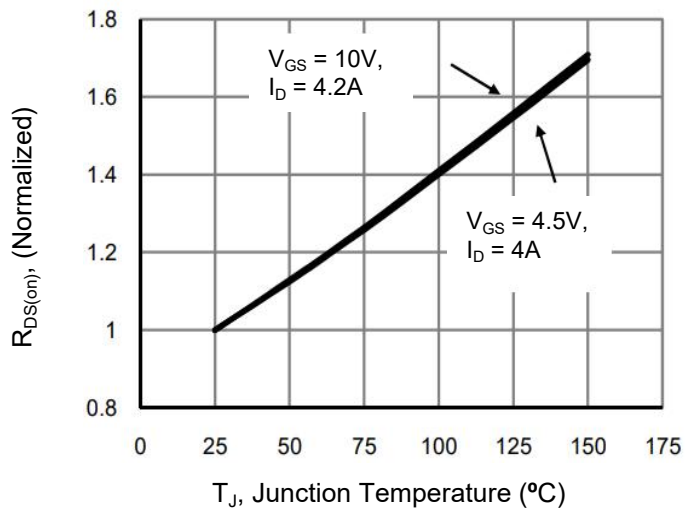


Figure 8. Safe Operation Area

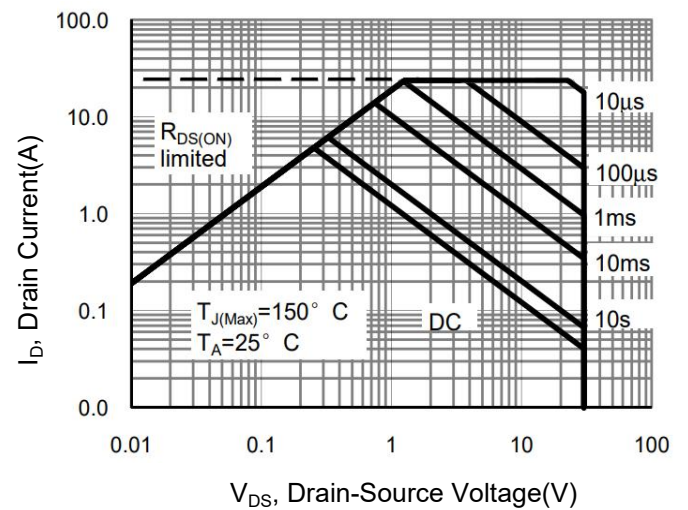
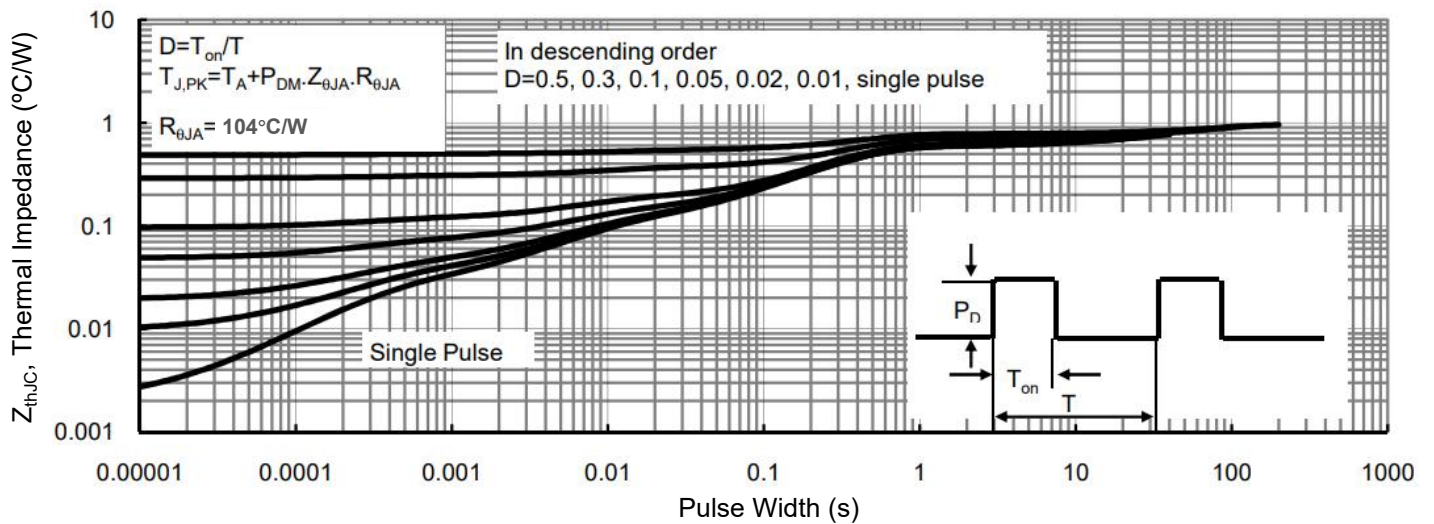
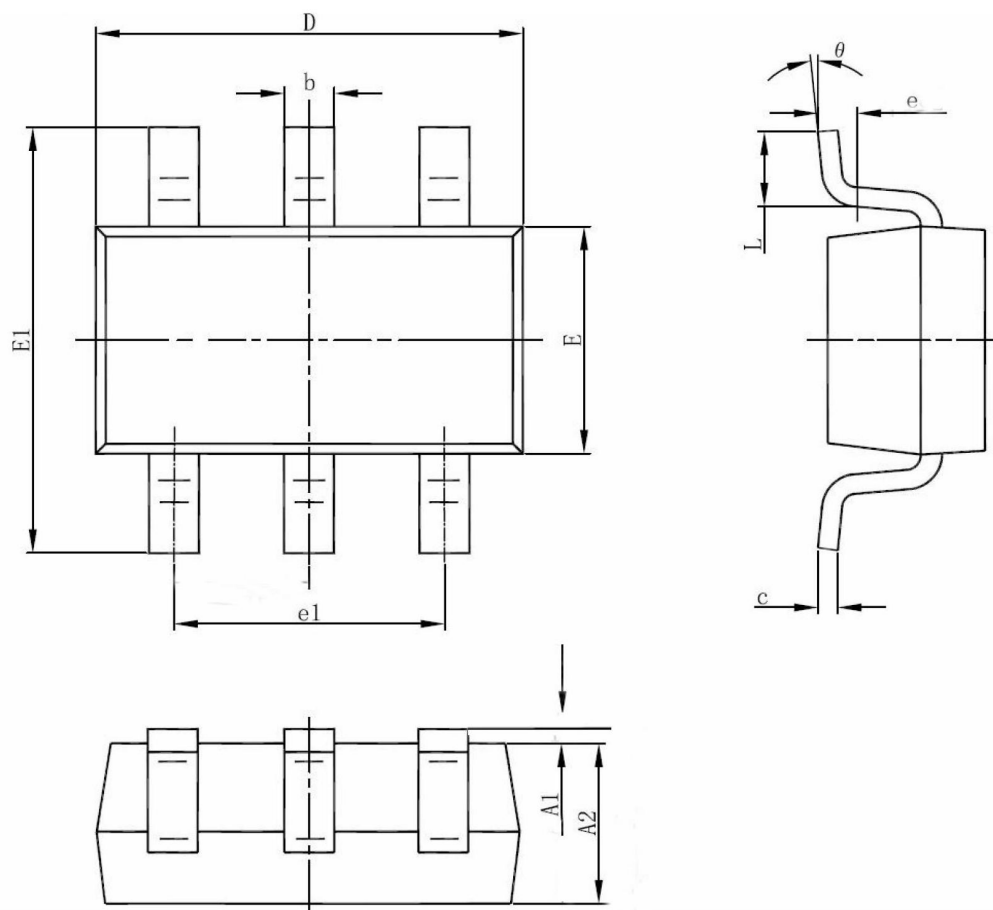


Figure 9. Normalized Maximum Transient Thermal Impedance



SOT-23-6L Package Information



DIM	MIN	NOM	MAX
A1	0.00	-	0.10
A2	1.00	1.10	1.20
b	0.30	0.40	0.50
c	0.10	0.15	0.20
D	2.80	2.90	3.00
E	1.50	1.60	1.70
E1	2.60	2.80	3.00
e	0.2GAUGE PLANE		
e1	-	1.90	-
L	0.30	0.45	0.60
θ	0°	-	8°
All Dimensions in mm			